



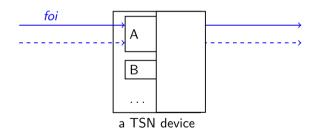
On Time Synchronization Issues in Time-Sensitive Networks with Regulators and Nonideal Clocks published in POMACS, June 2020, doi:10.1145/3392145

Ludovic Thomas and Jean-Yves Le Boudec ISAE-SUPAERO (France) and EPFL (Switzerland)

> 5th Workshop on Network Calculus 9 October 2020

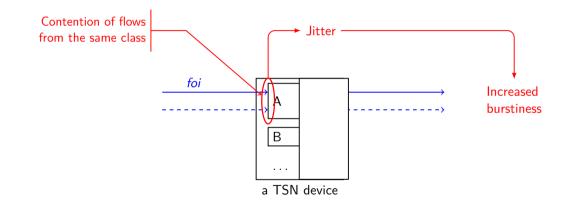
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TSN: Class-based schedulers. FIFO-per-class.



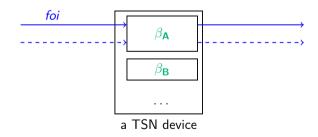
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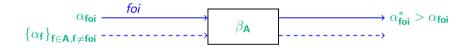
TSN: Class-based schedulers. FIFO-per-class. With Network Calculus (e.g. TFA [Schmitt and Zdarsky, 2006]):



TFA = Total Flow Analysis.

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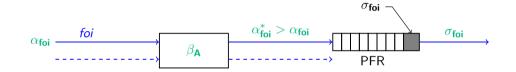
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On Time Synchronization Issues in Time-Sensitive Networks

WoNeCa-5. 9 October 2020 3 / 25

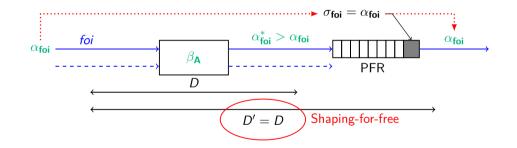
Traffic Shaping with a Per-Flow Regulator (PFR)

also called Packetized Greedy Shaper [Le Boudec and Thiran, 2001, Definition 1.7.6].



Traffic Shaping with a Per-Flow Regulator (PFR)

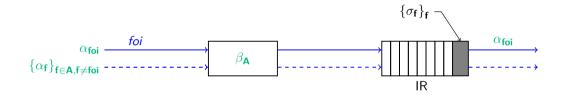
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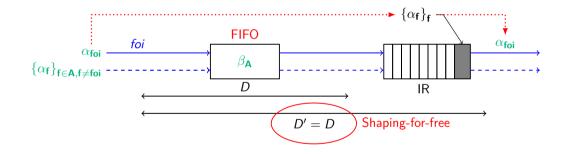
Traffic Shaping with an Interleaved Regulator (IR)

[Specht and Samii, 2016, Le Boudec, 2018]



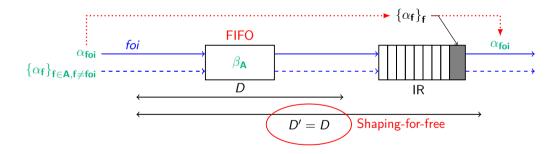
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Traffic Shaping with an Interleaved Regulator (IR)

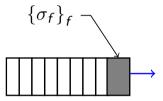
[Specht and Samii, 2016, Le Boudec, 2018]



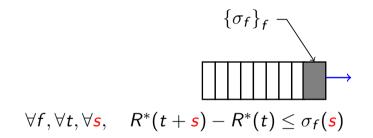
TSN Asynchronous Traffic Shaping (ATS) [IEEE, 2019] reproduces the IR in TSN.

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Regulators measure elapsed time

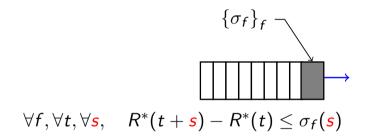


Regulators measure elapsed time



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Regulators measure elapsed time



Discussions in TSN ATS (Asynchronous Traffic Shaping) [IEEE, 2019].
In doi:10.1145/3392145: theoretical foundations to address the problem.

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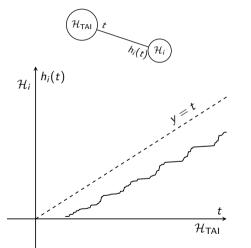
Contributions

 • Time model for
$$\begin{cases} non-synchronized \\ synchronized \end{cases}$$
 networks.

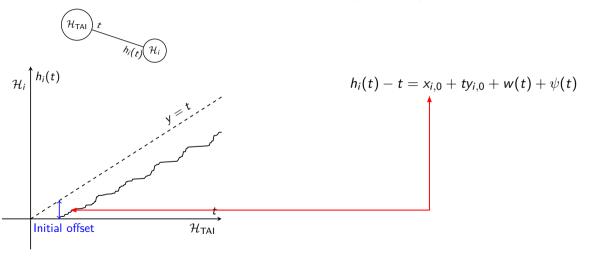
 • A toolbox of Network Calculus results for $\begin{cases} non-synchronized \\ synchronized \end{cases}$ networks.

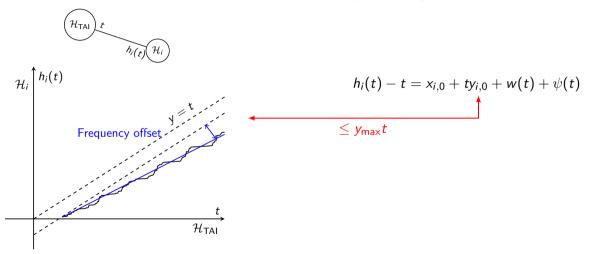
 • Analysis of regulators $\begin{cases} PFR \\ IR \end{cases}$ in $\begin{cases} non-synchronized \\ synchronized \end{cases}$ networks.

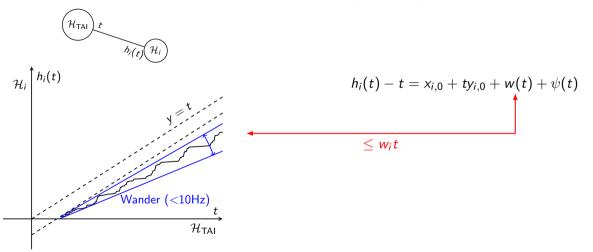
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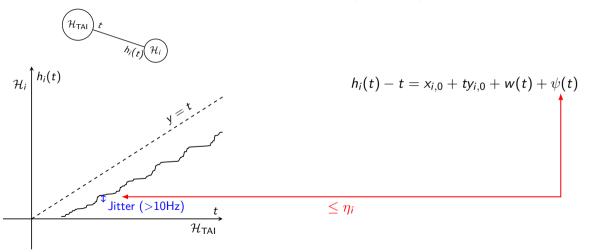
$$h_i(t) - t = x_{i,0} + ty_{i,0} + w(t) + \psi(t)$$

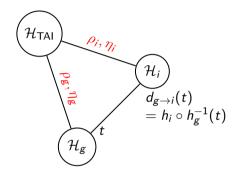


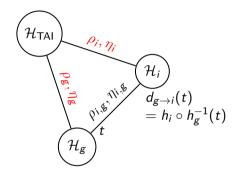


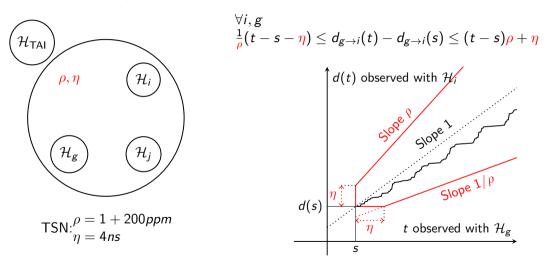


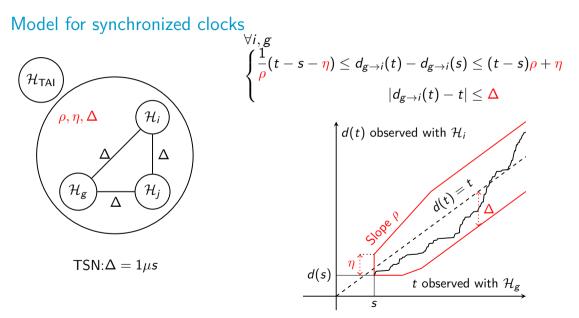
Definitions and terminology for synchronization networks [ITU, 1996]

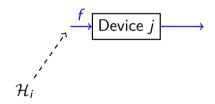


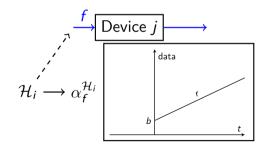




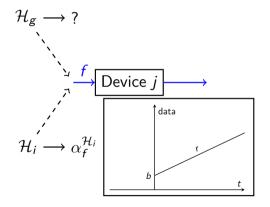




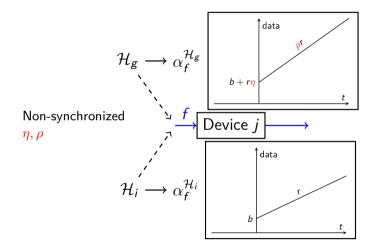


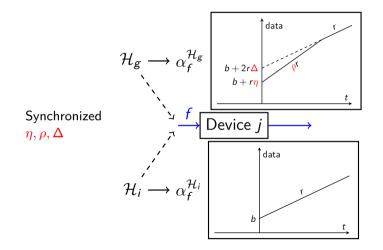


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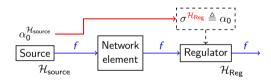
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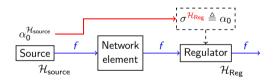
Instabilities with non-adapted regulators

Usual configuration of regulators = Non-adapted regulator



Instabilities with non-adapted regulators

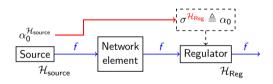
Usual configuration of regulators = Non-adapted regulator



Non-synchronized networks:
 Per-flow regulator
 Interleaved regulator

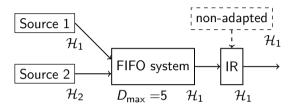
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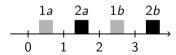
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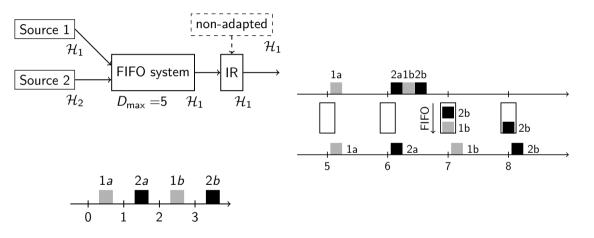
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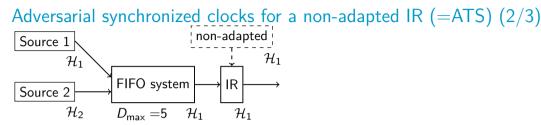
■ Synchronized networks: Per-flow regulator \rightarrow penalty $[\Delta, 4\Delta]$ Interleaved regulator \rightarrow unstable $\forall \Delta > 0$ Adversarial synchronized clocks for a non-adapted IR (=ATS) (1/3)

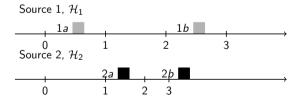


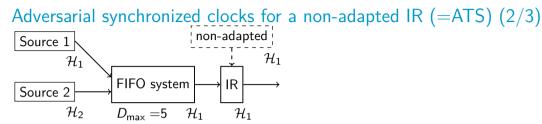


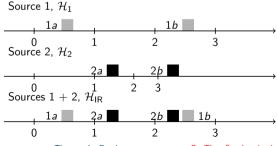
Adversarial synchronized clocks for a non-adapted IR (=ATS) (1/3)



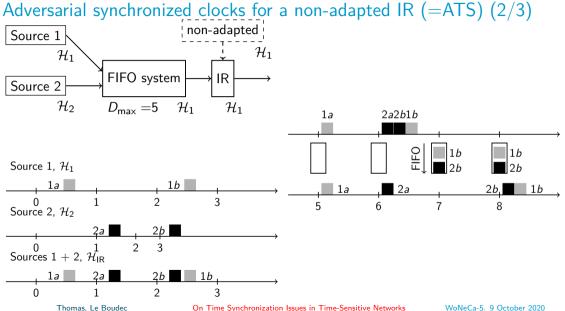








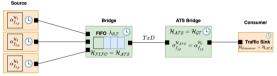




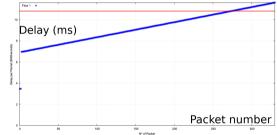
Adversarial synchronized clocks for a non-adapted IR (=ATS) (3/3)

Validation and extension through ns-3 simulations.

Example at low data rates:



- 3 sources @ 147 kB/s
- 1 queuing element @ 437.5 kB/s
- $\Delta = 1 \mu \mathrm{s}, \ \rho = 1 + 100 \mathrm{ppm}$
- using adversarial clocks
- ⇒ red line is Network Calculus delay bound assuming perfect clocks



Work by Guillermo Aguirre

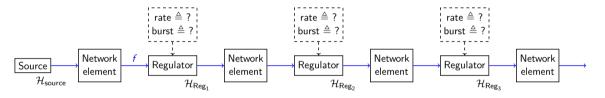
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Computing the configuration of regulators

How to configure the regulators ?

assuming leaky-bucket-constrained flows

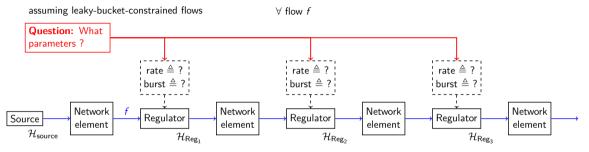
 \forall flow f



 $(\mathcal{H}_{\mathsf{TAI}})$

Computing the configuration of regulators

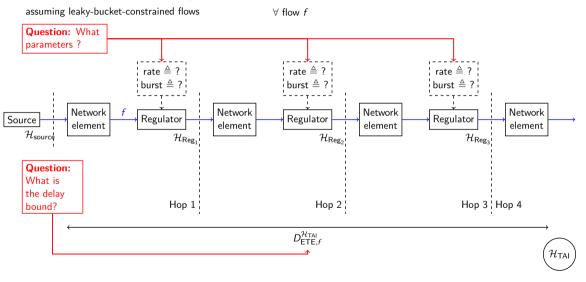
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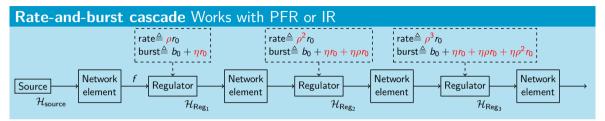
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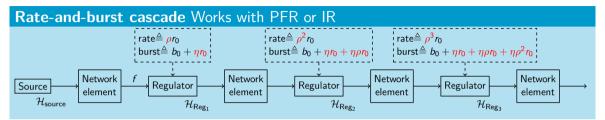


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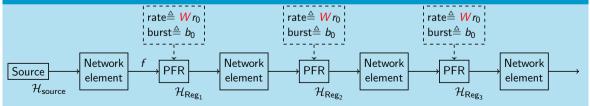
Two methods for synchronized and non-synchronized networks



Two methods for synchronized and non-synchronized networks



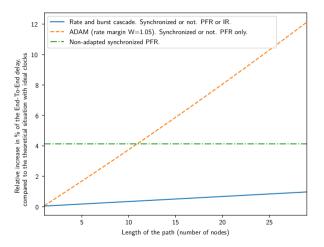
ADAM Works with PFR only



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Performance comparison

Performance comparison



Increase of the ETE delay bound wrt ideal clocks.

Conclusion

- **Time-model** for bounding the behavior of the clocks in the network.
- Instability of the non-adapted ATS regulator for any $\Delta > 0$.
- Two methods for **configuring the regulators** in a network, relying on a **Network Calculus toolbox**.

Conclusion

- **Time-model** for bounding the behavior of the clocks in the network.
- **Instability** of the non-adapted ATS regulator for any $\Delta > 0$.
- Two methods for **configuring the regulators** in a network, relying on a **Network Calculus toolbox**.

Future work:

- Improvements on the ADAM method.
- Simulation of different (more realistic) clock models in ns-3.
- The toolbox could be of interest when studying other technologies / TSN components.

Bibliography I

IEEE (2019).

Draft Standard for Local and Metropolitan Area Networks—Bridges and Bridged Networks—Amendment: Asynchronous Traffic Shaping.

IEEE P802.1Qcr/D2.0, In IEEE802.1 private repository. To obtain the access credentials, visit https://www.ietf.org/proceedings/52/slides/bridge-0/tsld003.htm or contact the IEEE802.1 chair.

http://www.ieee802.org/1/files/private/cr-drafts/d2/802-1Qcr-d2-0.pdf.

ITU (1996).

Definitions and terminology for synchronization networks. *ITU G.810*.

https://www.itu.int/rec/T-REC-G.810-199608-I/en.

Bibliography II

Le Boudec, J.-Y. (2018).

A Theory of Traffic Regulators for Deterministic Networks With Application to Interleaved Regulators.

IEEE/ACM Transactions on Networking, 26(6):2721-2733. http://doi.org/10.1109/TNET.2018.2875191.

 Le Boudec, J.-Y. and Thiran, P. (2001).
 Network Calculus: A Theory of Deterministic Queuing Systems for the Internet.
 Lecture Notes in Computer Science, Lect.Notes Computer. Tutorial. Springer-Verlag, Berlin Heidelberg.
 https://www.springer.com/us/book/9783540421849.

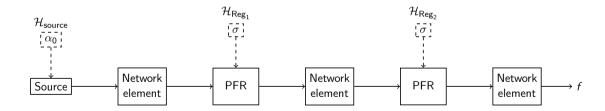
Bibliography III

Schmitt, J. B. and Zdarsky, F. A. (2006).

The DISCO network calculator: A toolbox for worst case analysis.

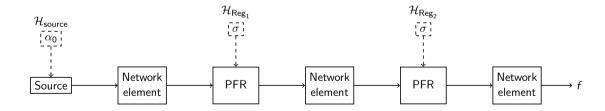
In Proceedings of the 1st International Conference on Performance Evaluation Methodolgies and Tools, Valuetools '06, pages 8–es, New York, NY, USA. Association for Computing Machinery. https://doi.org/10.1145/1190095.1190105.

Specht, J. and Samii, S. (2016). Urgency-Based Scheduler for Time-Sensitive Switched Ethernet Networks. In 2016 28th Euromicro Conference on Real-Time Systems (ECRTS), pages 75–85.

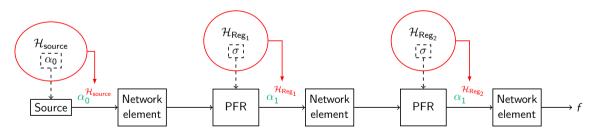


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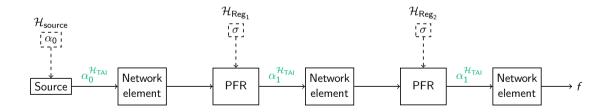
Step 1 (per flow): arrival curve at each network element



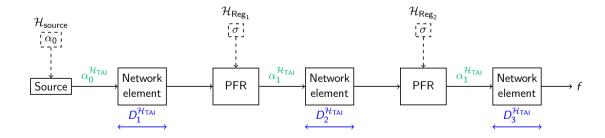
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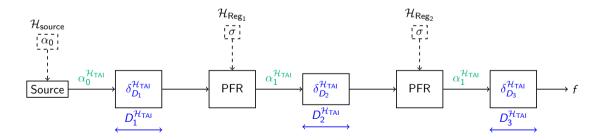
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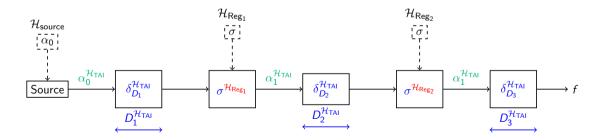
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- Step 2 (per node): compute delay bounds



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- Step 3 (per flow): service-curve characterization and end-to-end delay bound



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